**IMPROVING POWER SYSTEM STABILITY USING STATIC SYNCHRONOUS SERIES COMPENSATOR**

A PROJECT REPORT

**Submitted in partial fulfillment of the Requirement for the award of the degree of**

**BACHELOR OF TECHNOLOGY**

**IN**

**ELECTRICAL AND ELECTRONICS ENGINEERING**

**Submitted by**

**P.NAGAHAREESH (10F01A0292)**

**R.DIVYA (10F01A0296) N.ASHOK KUMAR (11F05A0216)**

**V.RANGANAYAKULU (10F01A02B1)**

**Under the esteemed guidance of**

**Mr.Y.NARAYANARAO M.Tech**

**(Associate Professor)**



**ST.ANN’S COLLEGE OF ENGINEERING & TECHNOLOGY**

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

(Accredited twice by **NBA**, **IE(I)Kolkata**,**& NAAC** With “**A”** GRADE approved

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